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Substitute for Form 1449B/PTO		Complete if Known
	Application Number	To Be Assigned 10/8/0525
INFORMATION DISCLOSURE	Filing Date	Herewith
STATEMENT BY APPLICANT	First Named Inventor	Kiyotaka Mori
(Use as many sheets as necessary)	Art Unit	To Be Assigned
	Examiner Name	To Be Assigned
SHEET 2 of 2	Attorney Docket No.	MATB-406US

		NON-PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T2
OK		AIST Announces World's Thinnest Vertical-Type Double-Gate MOSFET Using Newly Discovered Process, AIST Today International Edition No. 8, pp. 10-12, 2003.	
ch		MASSIMO LAZZAIR ET AL., Block Copolymers as a Tool for Nanomaterial Fabrication, Advanced Materials 15, No. 19, pp. 1583-1594, October 2, 2003.	
Examiner Signature		Date Considered 2-16-05	يـــــــــــــــــــــــــــــــــ

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